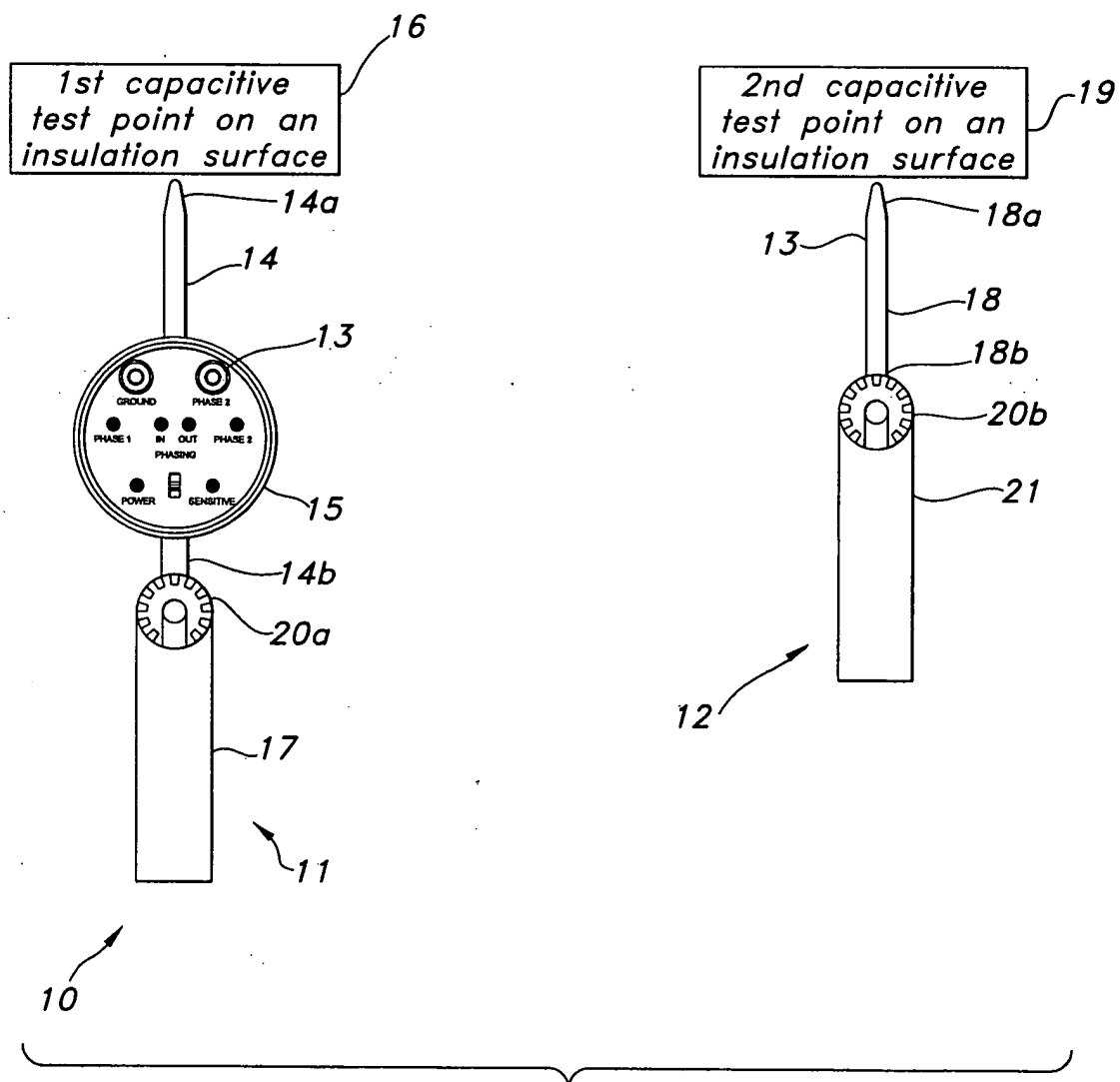
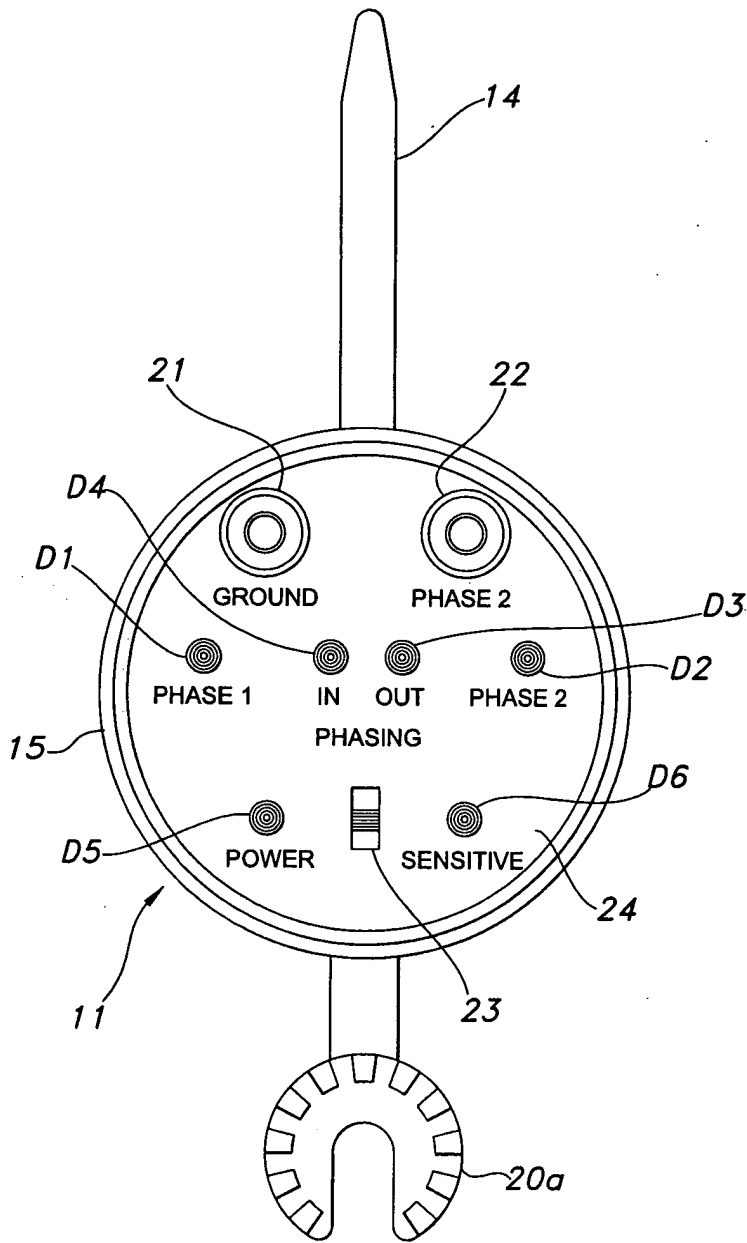


1/4

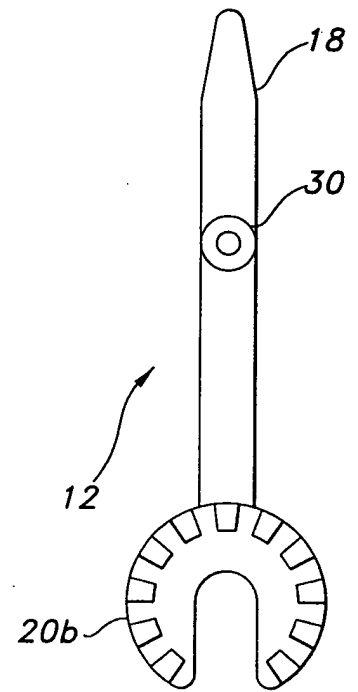


**FIG 1**

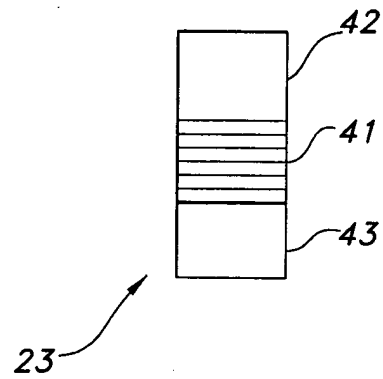
2/4



**FIG 2**

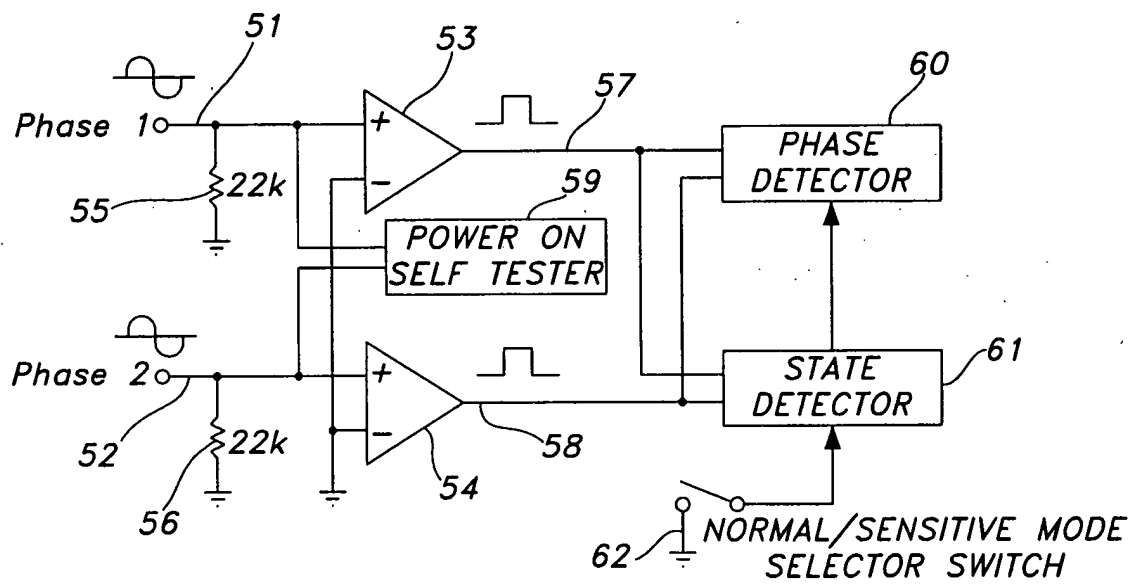


**FIG 3**



**FIG 4**

3/4



**FIG 5**

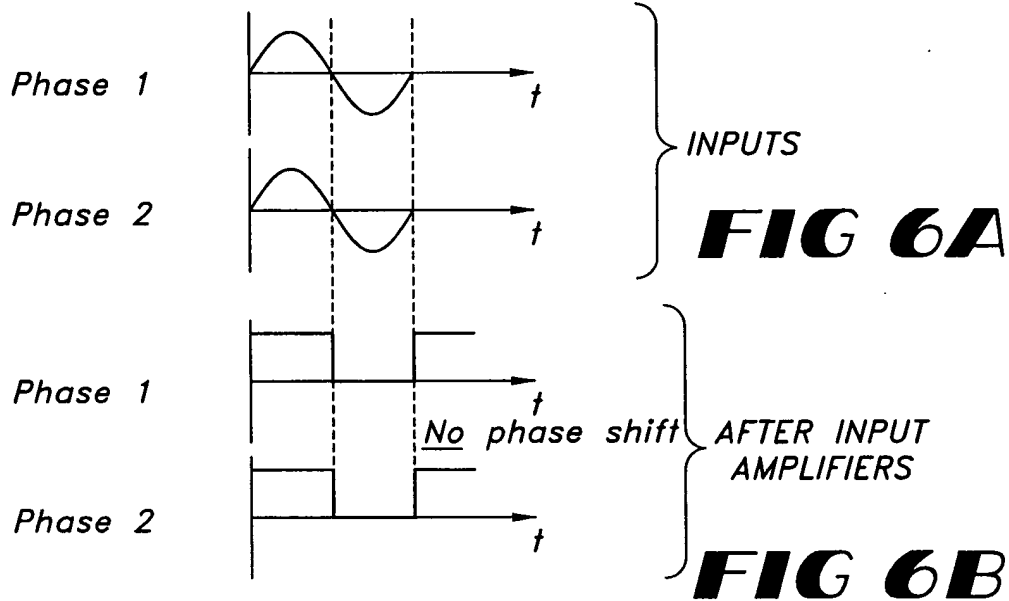
Title: CAPACITIVE TEST POINT VOLTAGE AND PHASING DETECTOR

Applicant: Timothy R. Taylor

Application No.: 10/033,118

4/4

WAVEFORMS FOR IN-PHASE:



WAVEFORMS FOR OUT-OF-PHASE:

